



Sheet: 2 of: 2

FORM: PTO-1449 (REV: 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			Atty Docket No: 95-0653.03	Serial No: 08/984,563
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Applicant: Mailoux et al.	
				Filing Date: December 3, 1999	Group: 2751

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Mar 02 1999
LL	AA 5,682,354	10/97	Manning	365	233.5	
LL	AB 5,640,364	06/97	Merritt et al.	365	233.5	
LL	AC 5,729,504	12/95	Cowles 03/1998	365	236	GROUP 2700
LL	AD 5,661,695	08/97	Zagar et al.	365	233.5	
LL	AE 5,305,284	04/94	Iwase	365	238.5	
LL	AF 5,325,330	06/94	Morgan	365	189.05	
AG						
AH						
AI						
AJ						
AK						

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GE 12/18/99

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes	Translation No
AL						<input type="checkbox"/>	<input type="checkbox"/>
AM						<input type="checkbox"/>	<input type="checkbox"/>
AN						<input type="checkbox"/>	<input type="checkbox"/>
AO						<input type="checkbox"/>	<input type="checkbox"/>
AP						<input type="checkbox"/>	<input type="checkbox"/>
AQ						<input type="checkbox"/>	<input type="checkbox"/>

Initial

OTHER ART (including author, title, date, pertinent pages, etc.)

NR			PCT Patent Application No. PCT/US95/16656, filed 12/21/1995, entitled: Burst Edo Memory Device Address Counter.
	AS		
	AT		

Examiner:	Date Considered:
H. Kim	3/12/99

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication with applicant.